

# H25X Absolute Optical Encoder

## Mechanical Specifications

- Shaft Diameter:** 3/8"
- Shaft Loading:** Up to 25 pounds axial and radial
- Shaft Runout:** 0.0005 T.I.R. at midpoint regardless of shaft diameter
- Starting Torque at 25°C:** With shaft seal 8 in-oz
- Bearings:** Class ABEC 7 standard, ABEC 5 for 1/2" shaft
- Shaft Material:** 416 stainless steel
- Bearing Housing:** Die cast aluminum with iridite finish
- Cover:** Die cast aluminum
- Bearing Life:** 2 X 10<sup>8</sup> revs (1300 hrs at 2500 RPM) at rated load 1 X 10<sup>10</sup> revs (67,000 hrs at 2500 RPM) at 10% of rated load
- Maximum RPM:** 12,000 RPM mechanical,
- Moment of Inertia:** 4.1 X 10<sup>-4</sup> oz-in-sec<sup>2</sup>
- Weight:** 13 oz typical

## Electrical Specifications

- Code:** 14 or 15 bits NB or GC
- Counts Per Shaft Turn:** 16,384 or 32,768
- Count Transition Accuracy:** ± 1/2 bit maximum
- Supply Voltage:** 5 VDC or 10-24 VDC
- Current Requirements:** 120 mA typical (10-24V operation), 400mA (5V operation)
- Output Formats:**  
Parallel: Gray Code, Natural Binary  
Serial: Serial Synchronous Interface (SSI) compatible
- Output Device:** (see note 5)  
7272: Line Driver, 5 – 24 VDC, V<sub>out</sub> = V<sub>in</sub>  
7273: Open Collector, accepts 5 – 24 VDC  
SSI: See page 40
- Protection Level:** Reverse, overvoltage and output short circuit protection (see note 5)
- Frequency Response:** 100kHz
- Output Termination Pinouts:** See table page 40

## Environmental Specs

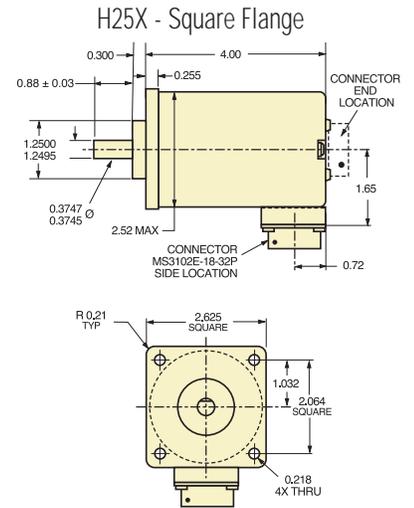
Reference the H25 Incremental Encoder, pages 16-17

## Connector

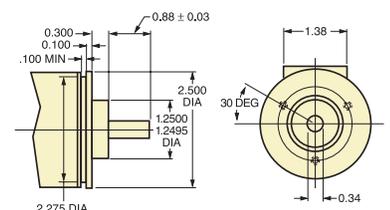
MS3112E14-19P, 19-pin connector on encoder body, mates to MS3116F14-19S (or equivalent)

**NOTES & TABLES:** All notes and tables referred to in the text can be found on pages 50 and 51.

The H25X series single turn encoder is designed for those applications that require 14 or 15 bits of resolution in a compact, easy-to-integrate package. Gray Code and Natural Binary outputs are available for installations using a parallel input with the controller. For simplicity of data transmission, ease of cabling and better noise immunity, an SSI (Synchronous Serial Output) is also offered. This encoder works with the BEI Serial-to-Parallel converter, allowing for system upgrades from parallel output to SSI.



## H25X - 2.50 Servo Mount



TOLERANCES: .XX = ± 0.01, .XXX = ± 0.005

The H25X is built to the exacting mechanical standards used with the H25 design, including: dual preloaded ABEC 7 bearings; matched thermal coefficients on critical components and electronically centered code disks for high accuracy and stability over a range of environments. Specify the H25X when you need high pointing accuracy and ruggedness in a 14 or 15 bit absolute encoder for your telescope, antenna, robotics, material handling or general industrial automation.

## Certifications



See Regulatory Information on pages 47-49 for further certification details.

Figure 1 Gray Code

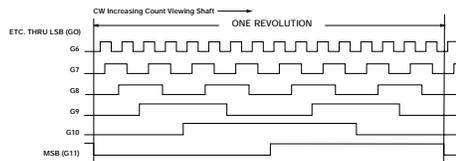
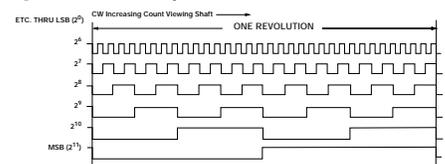


Figure 2 Natural Binary



# H25X Absolute Encoder Ordering Options

FOR ASSISTANCE CALL 800-350-2727

Use this diagram, working from left to right to construct your model number (example: H25XE-F4-SS-14GC-7272-CW-SM14/19).

All notes and tables referred to can be found on pages 50-51.

<b>H25X</b>								
<b>TYPE:</b> Heavy Duty 2.5 inch Dia. X = Extended Resolution	<b>FACE MOUNTS:</b> F4 = 6-32 holes, 3 places equally spaced on 2.00 inch bolt circle <i>See note 1</i>	<b>SHAFT SEAL CONFIGURATION:</b> SS = Shaft Seal <i>See note 2</i>	<b>CODE TYPE:</b> GC = Gray Code NB = Natural Binary	<b>DIRECTION CONTROL:</b> <i>See page 34</i>	<b>OUTPUT IC TYPE:</b> 7272 = Line Driver 7273 = Open Collector	<b>TERMINATION TYPE:</b> M14/19 = 19 pin connector <i>See page 41</i>	<b>SPECIAL FEATURES:</b> S = Special features specified on purchase order (consult factory) <i>See note 6</i>	
<b>HOUSING:</b> D = Square Flange E = 2.5 inch dia. servo		<b>NUMBER OF BITS:</b> 14 = 14-Bits, 16,384 counts per turn 15 = 15-Bits, 32,768 counts per turn	<b>OUTPUT IC TYPE:</b> S3 = Serial Synchronous Interface <i>See note 5 and page 40 for SSI</i>	<b>OUTPUT TERMINATION LOCATION:</b> E = End (5V only, 15 bit)				

# 16 | Model H25 Incremental Encoder

## Mechanical Specifications

- Shaft Diameter:** 3/8" (1/2" as special feature)
- Flat On Shaft:** 3/8" Shaft: 0.80 long X 0.03" deep;  
1/2" Shaft: 0.80 long X 0.04" deep
- Shaft Loading:** 3/8" shaft: Up to 40 pounds axial and 35 pounds radial; 1/2" shaft: Up to 90 pounds axial and 80 pounds radial
- Shaft Runout:** 0.0005 T.I.R. at midpoint regardless of shaft diameter
- Starting Torque at 25°C:** Without shaft seal 1.0 in-oz (max); With shaft seal 2.5 in-oz (max); 1/2" shaft with shaft seal: 3.5 in-oz (max)
- Bearings:** Class ABEC 7 standard, ABEC 5 for 1/2" shaft
- Shaft Material:** 416 stainless steel
- Bearing Housing:** Die cast aluminum with iridite finish
- Cover:** Die cast aluminum
- Bearing Life:** 2 X 10<sup>8</sup> revs (1300 hrs at 2500 RPM) at rated load 1 X 10<sup>10</sup> revs (67,000 hrs at 2500 RPM) at 10% of rated load
- Maximum RPM:** 12,000 RPM nominal, 8000 RPM with 1/2" shaft (see Frequency Response, below) 30,000 RPM available on units with 3/8" shaft – consult with factory
- Moment of Inertia:** 4.1 X 10<sup>-4</sup> oz-in-sec<sup>2</sup>; 5.2 X 10<sup>-4</sup> oz-in-sec<sup>2</sup> with 1/2" shaft
- Weight:** 13 oz typical, 14.5 oz typical with 1/2" shaft

## Electrical Specifications

- Code:** Incremental
- Output Format:** 2 channels in quadrature, 1/2 cycle index gated with negative B channel
- Cycles Per Shaft Turn:** 1 to 72,000 (see table 2) For resolutions above 3,600 see interpolation options on pages 32 and 33
- Supply Voltage:** 5 to 24 VDC available
- Current Requirements:** 100 mA typical +output load, 250 mA (max)
- Output Device:** (see note 5)  
4469: Line Driver, 5 – 15 VDC, V<sub>out</sub> = V<sub>in</sub>  
7272: Line Driver, 5 – 28 VDC, V<sub>out</sub> = V<sub>in</sub>  
7272: Line Driver, 5 – 28 VDC, V<sub>out</sub> = 5 VDC (special feature)  
7273: Open Collector, accepts 5 – 28 VDC
- Protection Level:** Reverse, overvoltage and output short circuit (see note 5)
- Frequency Response:** 100 kHz (see note 7)
- Output Terminations:** See table 1, page 51
- Note:** Consult factory for other electrical options

## Environmental Specifications

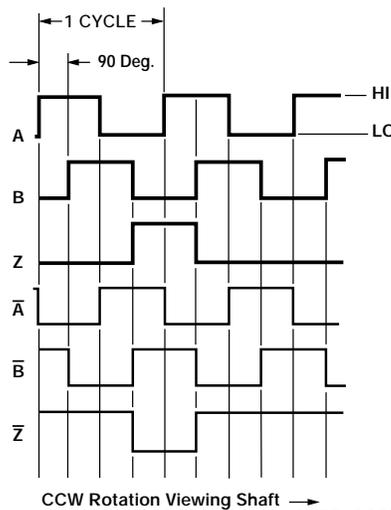
- Enclosure Rating:** NEMA 4 & 13 (IP 66) when ordered with shaft seal (on units with an MS connector) or a cable gland (on units with cable termination).
- Temperature:** Operating, 0° to 70° C; extended temperature testing available (see note 8); Storage, -25° to 90° C unless extended temperature option called out.
- Shock:** 50 g's for 11 msec duration
- Vibration:** 5 to 2000 Hz @ 20 g's
- Humidity:** 98% RH without condensation

**NOTES & TABLES:** All notes and tables referred to in the text can be found on pages 50 and 51.



The H25 is the flagship of the BEI Industrial Encoder Division product line. It was designed from the ground up for the industrial machine tool marketplace. The H25 offers features such as EMI shielding, 40 lb. ABEC 7 bearings, matched thermal coefficients on critical components, and custom high-efficiency optics. The encoder meets NEMA 4 and 13 requirements when ordered with the shaft seal. Typical applications include machine control, process control, the wood processing industry, oil well logging, industrial weighing, agricultural machinery, textile equipment, web process control, robotics, and food processing.

**Figure 1** Output Waveform



## Certifications

The H25 Incremental Encoder is available with the following certifications:

-  EN 55011 and EN 61000-6-2
-  CENELEC EEX ia IIB T4 and EEX ia IIC T4
-  U.S. Standards Class I, Group A,B,C & D; Class II Group E,F & G
-  Canadian Standards Class I, Zone 0, Group IIB & IIC
-  CSA Class I, Div 1 Group C&D

See Regulatory Information on pages 47–49 for further certification details.

# 40 | Absolute Encoder Options

## Parallel Absolute Output

The two most common types of absolute outputs are the Gray Code and the Natural Binary. Resolution for absolute encoders is expressed in "bits" where each successive bit increases the resolution by a factor of two. For example, 10 bits =  $2^{10}$  = 1024 counts per revolution.

Natural binary code (Figure 1) is constructed so that the code counts up using the natural sequence of binary counting, i.e. 000, 001, 010, 011, 100 . . . etc. The drawback to using this code sequence is that at several count positions the code will have transitions on multiple bits simultaneously. Due to the normal variations caused by gate delays, line impedances, etc. the actual transitions will not occur simultaneously. Reading data during one of these times could result in an erroneous reading. This can be overcome by taking multiple readings.

Gray code (Figure 2), by contrast, is designed to avoid the multiple transition problem entirely. It is specifically constructed so that only one bit will transition at a time. This ensures that state changes are much less ambiguous to the controller and is generally considered to be a more robust type of absolute code.

Regardless of the code type, one of the characteristics of absolute encoders is that they can readily be used for any resolution up to and including their maximum resolution. For example, a 12 bit encoder can be used at only 8 bits by ignoring (or disconnecting) the four lowest significant bits (LSB). This enables an installation that uses multiple absolute encoders to use the same encoder throughout with each controller using only the bits that it needs.

Figure 1 Natural Binary

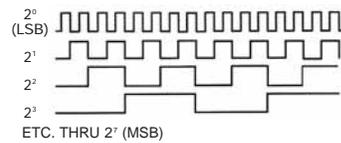
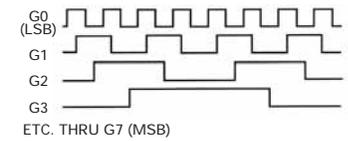


Figure 2 Gray Code



## Ordering 8-Bit Absolutes

For years, we produced encoders with a maximum resolution of 8 bits. Lots of those old 8 bit encoders are still around. We update them to newer 12 bit designs on a case-by-case basis. If you have an 8 bit encoder, here is how that model number was constructed: **Direction of Rotation, Count, Code** and **Latch** designators were inserted between **Shaft Seal Configuration** and **Output IC** as shown below. To specify an equivalent encoder based on the 12 bit design, please call our Applications Specialists at **800-ENCODER** (800-362-6337) or check our web site at **www.beiied.com**.

**Direction of Rotation:** CCW or CW

**Count:** 8

**Code:** GC= Gray Code or NB= Natural Binary

**Latch:** L= Latch or Blank=None

**Output Terminations:** EM20=MS3102R20-29P or ED25=DB25P; SM18 = MS3102R18-1P; C18 = Cable, with length specified in inches. Specify ED25 for Line Driver Outputs.

**Example: H25E-F1-SS-CCW-8GC-7406R-EM20**

(one possible encoder configuration with the 8-Bit Absolute Option.)

## Serial Synchronous Interface (SSI)

SSI output provides effective synchronization in a closed-loop control system.

A clock pulse train from a controller is used to clock out sensor data: one bit of position data is transmitted to the controller per one clock pulse received by the sensor. The use of a differential driver permits reliable transmission of data over long distances in environments that may be electrically noisy. The encoder utilizes a clock signal, provided by the user interface, to time the data transmission. Receiving electronics must include an appropriate receiver as well as line terminating resistors.

### Features

- Synchronous transmission
- Transmission lengths to 1000 feet
- Accepts clock rates from 100 KHz to 1.8 MHz

### Data Transmission Sequence

1. Output driver of the encoder is a MAX 491 transceiver in transmit mode. The recommended receiver is a MAX 491 transceiver in receive mode.
2. Controller provides a series of pulses (or differential pulse pairs) on the CLOCK input lines.
3. On the first HIGH-to-LOW CLOCK transition, the encoder latches its data at the current position and prepares to transmit.
4. Controller reads data on the falling edge of the next 16 clock cycles.
5. The first bit is a START bit and is always HIGH.
6. Next come 12 data bits beginning with the most significant bit (MSB) and ending with the least significant bit (LSB). This is followed by three LOW pulses.
7. After the DATA bits, the DATA line goes LOW and remains LOW for a minimum of 30 microseconds between the end of the DATA bits and the beginning of the next CLOCK series.

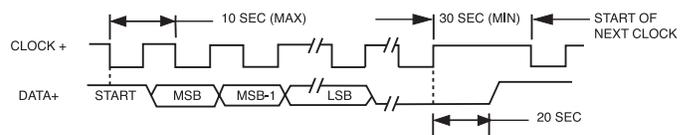
### Interfacing Long Data Lines

Cable impedance can create a transmission delay, in effect, shifting the phase relationship between the clock pulse and the data. If this phase shift exceeds 180°, then the wrong bit position will be sampled by the receiver. As a result, the maximum allowable clock frequency is a function of the cable length. For 24 AWG, stranded, 3 pair cable (BEI part number 37048-003 or equivalent) the group delay is 1.36ns/ft. The table below shows the maximum transmission rate allowable as a function of cable length to ensure a phase shift of less than 90°.

CLOCK, Maximum (kHz) = 92,000 / Cable Length (ft)CW

Cable Length (ft)	50	100	200	300	500	1000
Max Freq (kHz)	1800	900	500	300	200	100

### SSI Timing



## Ordering SSI

HOW TO SPECIFY SSI OUTPUT IN THE ENCODER MODEL NUMBER:

Use the designation, S3 between the **Code Format** designation and the **Connector** designation.

**Example: H25D-SS-12GC-S3-CW-SM18**



## Single Turn Absolute Encoder Options

The tables below are reference for pinouts, connections and operation of BEI's single turn absolute encoders. These absolute options are available in a wide range of package styles with a variety of outputs. The applicability table below shows which combinations are currently available. As always, you can call us at **800-360-ASAP (2727)** for immediate applications assistance should you have any questions.

Output Code and Terminations (12 & 13 Bit)								
PARALLEL CODE					TERMINATION TYPE			
	Gray Code		Natural Binary		Binary Coded Decimal	Cable	Conn	Term Board H38 & H40
	12 Bit	13 Bit	12 Bit	13 Bit				
MSB	G <sub>11</sub>	G <sub>12</sub>	2 <sup>11</sup>	2 <sup>12</sup>	A <sub>0</sub>	WHT/BLK	A	1
	G <sub>10</sub>	G <sub>11</sub>	2 <sup>10</sup>	2 <sup>11</sup>	B <sub>0</sub>	WHT/BRN	B	2
	G <sub>9</sub>	G <sub>10</sub>	2 <sup>9</sup>	2 <sup>10</sup>	C <sub>0</sub>	WHT/RED	C	3
	G <sub>8</sub>	G <sub>9</sub>	2 <sup>8</sup>	2 <sup>9</sup>	D <sub>0</sub>	WHT/ORN	D	4
	G <sub>7</sub>	G <sub>8</sub>	2 <sup>7</sup>	2 <sup>8</sup>	A <sub>1</sub>	WHT/YEL	E	5
	G <sub>6</sub>	G <sub>7</sub>	2 <sup>6</sup>	2 <sup>7</sup>	B <sub>1</sub>	WHT/GRN	F	6
	G <sub>5</sub>	G <sub>6</sub>	2 <sup>5</sup>	2 <sup>6</sup>	C <sub>1</sub>	WHT/BLU	G	7
	G <sub>4</sub>	G <sub>5</sub>	2 <sup>4</sup>	2 <sup>5</sup>	D <sub>1</sub>	WHT/IO	H	8
	G <sub>3</sub>	G <sub>4</sub>	2 <sup>3</sup>	2 <sup>4</sup>	A <sub>2</sub>	WHT/GRY	J	9
	G <sub>2</sub>	G <sub>3</sub>	2 <sup>2</sup>	2 <sup>3</sup>	B <sub>2</sub>	WHT	K	10
LSB <sub>12</sub>	G <sub>1</sub>	G <sub>2</sub>	2 <sup>1</sup>	2 <sup>2</sup>	C <sub>2</sub>	GRY/BLK	L	11
	G <sub>0</sub>	G <sub>1</sub>	2 <sup>0</sup>	2 <sup>1</sup>	D <sub>2</sub>	GRY/BRN	M	12
LSB <sub>13</sub>		G <sub>0</sub>		2 <sup>0</sup>	A <sub>3</sub>	GRY/RED	N	13
*0V (CIRCUIT COMMON)					B <sub>3</sub>	GRY/RED	P	
DIRECTION CONTROL						ORN	R	18
CASE GROUND						GRN	S	16
0 V (CIRCUIT COMMON)						BLK	T	15
LATCH CONTROL						YEL	U	17
+V (SUPPLY VOLTAGE)						RED	V	14
SHIELD DRAIN						BARE	—	

\*Pin P is available for a tri-state option

Output Applicability Table							
	12 BITS	13 BITS	14/15 BITS	12x12 BITS	SSI	4-20 mA	0-10 V
H25	•	•			•	•	•
H25X			•				
HS35	•				•		
H38	•	•		•	•	•	•
H40	•	•		•	•	•	•
HMT25				•	•		

**Direction Control:** Standard is CW increasing when viewed from the shaft end. Pin R is normally HI (or N/C) and is pulled up internally to +V. To reverse the count direction, Pin R must be pulled LO (COMMON).

**Latch control:** Encoder outputs are active and provide continuous parallel position information when Pin U is HI (or N/C). Pin U is pulled up internally to +V. When Pin U is LO (COMMON) the encoder outputs are latched at the logic state that is present when the latch is applied and will stay latched until Pin U is no longer grounded.

**Dir/Latch on 15-Bit Encoders:** Due to a limited number of connector pins, either direction control or latch is available on pin U.

Parallel Code (14 & 15 Bit)					
	Gray Code		Natural Binary		M14/19 Connector
	14 BIT	15 Bit	14 BIT	15 Bit	
LSB	G <sub>0</sub>	G <sub>0</sub>	2 <sup>0</sup>	2 <sup>0</sup>	A
	G <sub>1</sub>	G <sub>1</sub>	2 <sup>1</sup>	2 <sup>1</sup>	B
	G <sub>2</sub>	G <sub>2</sub>	2 <sup>2</sup>	2 <sup>2</sup>	C
	G <sub>3</sub>	G <sub>3</sub>	2 <sup>3</sup>	2 <sup>3</sup>	D
	G <sub>4</sub>	G <sub>4</sub>	2 <sup>4</sup>	2 <sup>4</sup>	E
	G <sub>5</sub>	G <sub>5</sub>	2 <sup>5</sup>	2 <sup>5</sup>	F
	G <sub>6</sub>	G <sub>6</sub>	2 <sup>6</sup>	2 <sup>6</sup>	G
	G <sub>7</sub>	G <sub>7</sub>	2 <sup>7</sup>	2 <sup>7</sup>	H
	G <sub>8</sub>	G <sub>8</sub>	2 <sup>8</sup>	2 <sup>8</sup>	J
	G <sub>9</sub>	G <sub>9</sub>	2 <sup>9</sup>	2 <sup>9</sup>	K
	G <sub>10</sub>	G <sub>10</sub>	2 <sup>10</sup>	2 <sup>10</sup>	L
	G <sub>11</sub>	G <sub>11</sub>	2 <sup>11</sup>	2 <sup>11</sup>	M
MSB14	G <sub>12</sub>	G <sub>12</sub>	2 <sup>12</sup>	2 <sup>12</sup>	N
	G <sub>13</sub>	G <sub>13</sub>	2 <sup>13</sup>	2 <sup>13</sup>	P
MSB15	DIR CONTROL	G <sub>14</sub>	DIR CONTROL	2 <sup>14</sup>	R
	CASE GROUND	CASE GROUND	CASE GROUND	CASE GROUND	S
	CIRCUIT COMMON	CIRCUIT COMMON	CIRCUIT COMMON	CIRCUIT COMMON	T
	LATCH	DIR/LATCH	LATCH	DIR/LATCH	U
	+V SUPPLY VOLTAGE	+V SUPPLY VOLTAGE	+V SUPPLY VOLTAGE	+V SUPPLY VOLTAGE	V

SSI Output Termination Table					
	M18 CONN	M14/19 CONN	CABLE CONN	TERM. BOARD	
				H38	H48
DATA +	A	A	YEL	4	1
DATA-	H	B	WHT/YEL	7	7
CLOCK+	B	C	BLU	5	2
CLOCK-	I	D	WHT/BLU	8	8
DIR CONTROL	C	R	ORN	6	3
CASE GROUND	G	S	BARE/SHIELD	1	6
CIRCUIT COMMON	F	T	BLK	2	5
+V SUPPLY VOLTAGE	D	V	RED	3	4
SHIELD DRAIN	—	—	BARE	—	—

**M18 Connector** is a MS3102E18-1P, 10-pin connector on the encoder body and mates to an MS3106F18-1S connector or can be used with a standard cable/connector assembly, BEI P/N 924-31186-18XX (Where X = 10, 20 or 30 for a 10, 20, or 30 foot length). This is the preferred connector for SSI output.

**M14/19 Connector** is a MS3112E14-19P, 19-pin connector on the encoder body and mates to an MS3114E14-19S or equivalent.