

H40 Absolute Shock Proof Encoder

SUNSTAR 传感与控制 <http://www.sensor-ic.com/> TEL: 0755-83376489 FAX: 0755-83376182 E-MAIL: szss20@163.com



Built to the same rugged standards as the H40 Incremental Heavy Duty encoder, this unit features an absolute encoder output up to 13 bits of resolution. Designed to take the rigors of physically demanding environments, the H40 has a heavy-duty housing, a 100+ pound bearing, and internal shock absorbers. When you need absolute position in a really tough environment, the H40 absolute is what you need.

Electrical Specifications

Reference the H25 Absolute Encoder, page 34

Mechanical & Environmental Specs

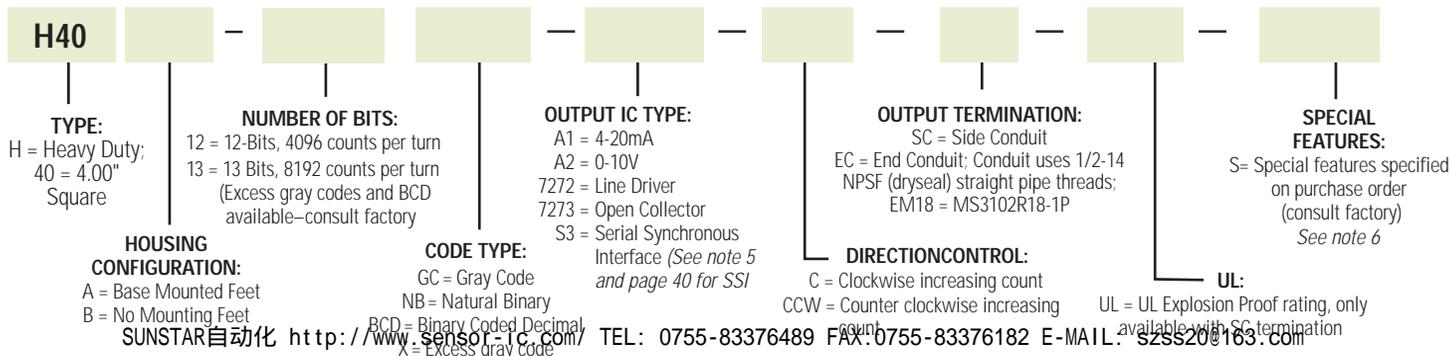
Reference the H40 Incremental Encoder, pages 28-29

H40 Absolute Encoder Ordering Options

FOR ASSISTANCE CALL 800-350-2727

Use this diagram, working from left to right to construct your model number (example: H40A-12GC-7272-CW-SC-UL).

All notes and tables referred to can be found on pages 50-51.



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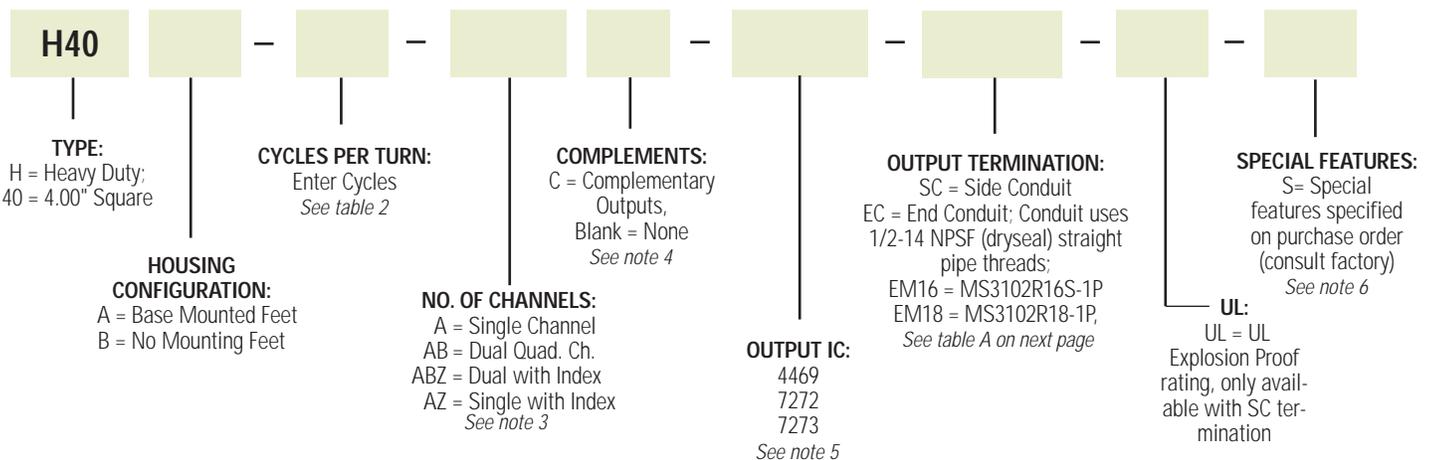
28 | H40 Shock Proof Optical Encoder



The H40 is an ultra heavy duty encoder whose internal structure is totally isolated from severe shock and shaft loading conditions. The optics and electronics are supported in shock absorbing material within the heavy cast outer housing. The encoder shaft is flexibly coupled to the high load capacity bearings and shaft assembly, which is carried in the outer housing. The entire bearing assembly is field-removable to permit proper shaft support while pressing pulleys, gears, etc. onto the shaft. An Underwriters Laboratories listed version of this model is available.

H40 Shock Proof Ordering Options FOR ASSISTANCE CALL 800-350-2727

Use this diagram, working from left to right to construct your model number (example: H40A-2000-ABZC-4469-SC-UL). All notes and tables referred to can be found on pages 50-51.

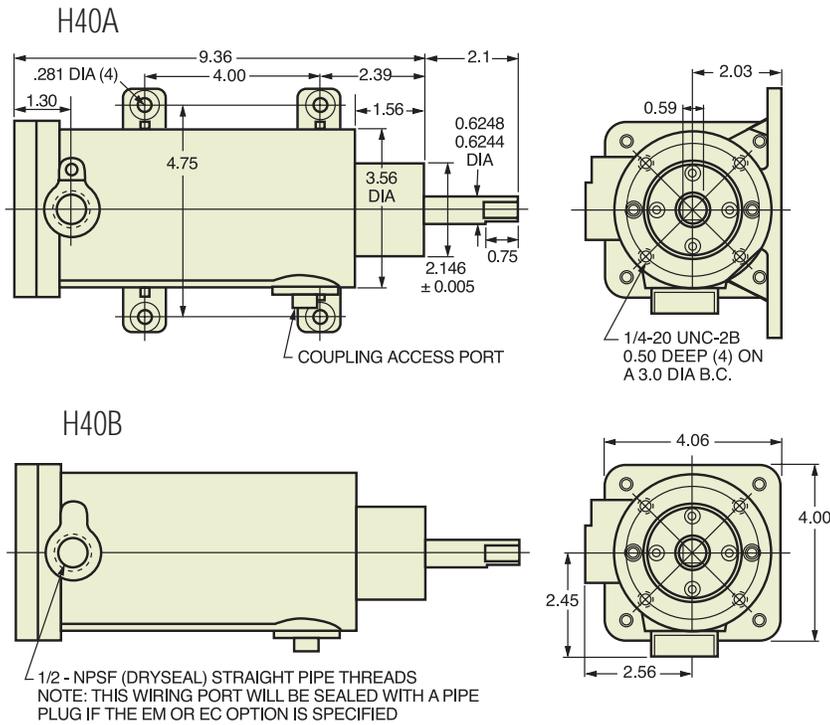


Certifications

The H40 Shock Proof Encoder is available with the following certifications:



See Regulatory Information on pages 47-49 for further certification details.



Field Replaceable Coupling and Bearing Assembly

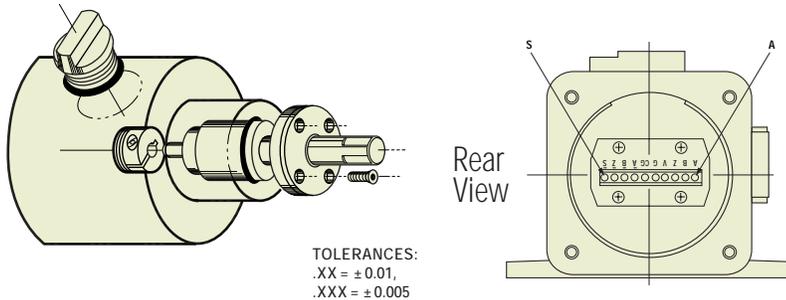


Table A Output Functions

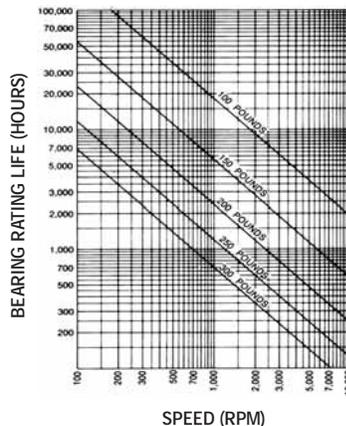
INCREMENTAL		8-BIT ABSOLUTE*		
TERMINAL	FUNCTION	GRAY CODE	NATURAL BINARY	TERMINAL NUMBER
A	CHANNEL A	G0	2 ⁰	1
B	B	G1	2 ¹	2
Z	Z	G2	2 ²	3
V	+VDC	G3	2 ³	4
G	GROUND (0V)	G4	2 ⁴	5
CG	CASE GROUND	G5	2 ⁵	6
A̅	A̅	G6	2 ⁶	7
B̅	B̅	G7	2 ⁷	8
Z̅	Z̅	SPARE		9
S	SPARE*	SPARE		10
		LATCH		11
		INTERROGATE		12
		SPARE		13
		+VDC		14
		0V (COMMON)		15
		CASE GROUND		16

Latch and Interrogate are optional

*For higher resolutions, see Absolute Options pages 40-41

Figure 1

Bearing Life vs. Speed at Various Radial Loads



Mechanical Specifications

- Shaft Diameter:** 5/8" nominal
- Flats On Shaft:** Two flats, 0.75" long X 0.30" deep at 90°
- Shaft Loading/Bearing Life:** Refer to Figure 1
- Shaft Runout:** 0.001" T.I.R. at mid-point of shaft
- Starting Torque at 25° C:** 10.0 in-oz (max)
- Bearings:** Class 52100 SAE high carbon steel
- Shaft Material:** 1070 carbon steel, 303 stainless steel optional
- Enclosure:** Die cast aluminum, hard anodized with dichromate sealed finish. Shaft seals and sealed bearings are standard to achieve environmental ratings.
- Maximum RPM:** 10,000 RPM (see Frequency Response, below)
- Coupling Windup:** The H40 uses an internal coupling. Windup error (degrees) = $\alpha \times 7.5 \times 10^{-4} \text{ rad/sec}^2$ where α = angular acceleration in rad/sec²
- Weight:** Approx 9 lbs

Electrical Specifications

- Code:** Incremental or Absolute (see Absolute options, pgs 40-41)
- Output Format:** 2 channels in quadrature, 1/2 cycle index gated with negative B channel or Absolute to 13 bits
- Cycles per Shaft Turn:** 1 to 72,000 (see table 2). For resolutions above 3,600 see interpolation options on pages 32 and 33; Absolute to 8192 counts per turn
- Supply Voltage:** 5 to 24 VDC available
- Current Requirements:** 100 mA typical +output load, 250 mA (max)
- Output Device:** (see note 5)
 - 4469: Line Driver, 5 – 15 VDC, $V_{out} = V_{in}$
 - 7272: Line Driver, 5 – 28 VDC, $V_{out} = V_{in}$
 - 7272: Line Driver, 5 – 28 VDC, $V_{out} = 5 \text{ VDC}$ (special feature)
 - 7273: Open Collector, accepts 5 – 28 VDC

- Protection Level:** Reverse, overvoltage and output short circuit (see note 5)
- Frequency Response:** 100 KHz (see note 7)
- Output Terminations:** See Table A, this page
- Termination Type:** Compression type, UL recognized. Accepts AWG 14 to 22, stranded wire, strip 1/4"
- Note:** Consult factory for other electrical options

Environmental Specifications

- Enclosure Rating:** NEMA 4 X & 6 (IP66), outdoor Non-Hazardous locations, NEMA 4 X & 13 (IP66), indoor Non-Hazardous locations
- Hazardous Area Rating:** The optional Underwriters Laboratories listed version is for use in hazardous locations; NEMA Enclosure 7, Class 1, Group D, Division 1, NEC Class 2 circuits only
- Temperature:** Operating, 0° to 70° C; extended temperature testing available (see note 8, pg 50); storage: -25° to 90° C unless extended temperature option called out
- Shock:** 200 g's at 11msec
- Vibration:** 5 to 2000 Hz @ 20 g's
- Humidity:** 100% RH

NOTES & TABLES: All notes and tables referred to in the text can be found on pages 50 and 51

40 | Absolute Encoder Options

Parallel Absolute Output

The two most common types of absolute outputs are the Gray Code and the Natural Binary. Resolution for absolute encoders is expressed in "bits" where each successive bit increases the resolution by a factor of two. For example, 10 bits = 2^{10} = 1024 counts per revolution.

Natural binary code (Figure 1) is constructed so that the code counts up using the natural sequence of binary counting, i.e. 000, 001, 010, 011, 100 . . . etc. The drawback to using this code sequence is that at several count positions the code will have transitions on multiple bits simultaneously. Due to the normal variations caused by gate delays, line impedances, etc. the actual transitions will not occur simultaneously. Reading data during one of these times could result in an erroneous reading. This can be overcome by taking multiple readings.

Gray code (Figure 2), by contrast, is designed to avoid the multiple transition problem entirely. It is specifically constructed so that only one bit will transition at a time. This ensures that state changes are much less ambiguous to the controller and is generally considered to be a more robust type of absolute code.

Regardless of the code type, one of the characteristics of absolute encoders is that they can readily be used for any resolution up to and including their maximum resolution. For example, a 12 bit encoder can be used at only 8 bits by ignoring (or disconnecting) the four lowest significant bits (LSB). This enables an installation that uses multiple absolute encoders to use the same encoder throughout with each controller using only the bits that it needs.

Figure 1 Natural Binary

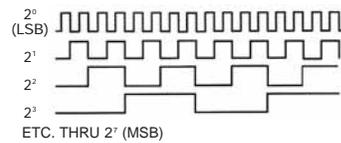
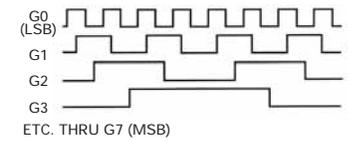


Figure 2 Gray Code



Ordering 8-Bit Absolutes

For years, we produced encoders with a maximum resolution of 8 bits. Lots of those old 8 bit encoders are still around. We update them to newer 12 bit designs on a case-by-case basis. If you have an 8 bit encoder, here is how that model number was constructed: **Direction of Rotation, Count, Code** and **Latch** designators were inserted between **Shaft Seal Configuration** and **Output IC** as shown below. To specify an equivalent encoder based on the 12 bit design, please call our Applications Specialists at **800-ENCODER** (800-362-6337) or check our web site at www.beiied.com.

Direction of Rotation: CCW or CW

Count: 8

Code: GC= Gray Code or NB= Natural Binary

Latch: L= Latch or Blank=None

Output Terminations: EM20=MS3102R20-29P or ED25=DB25P; SM18 = MS3102R18-1P; C18 = Cable, with length specified in inches. Specify ED25 for Line Driver Outputs.

Example: H25E-F1-SS-CCW-8GC-7406R-EM20

(one possible encoder configuration with the 8-Bit Absolute Option.)

Serial Synchronous Interface (SSI)

SSI output provides effective synchronization in a closed-loop control system.

A clock pulse train from a controller is used to clock out sensor data: one bit of position data is transmitted to the controller per one clock pulse received by the sensor. The use of a differential driver permits reliable transmission of data over long distances in environments that may be electrically noisy. The encoder utilizes a clock signal, provided by the user interface, to time the data transmission. Receiving electronics must include an appropriate receiver as well as line terminating resistors.

Features

- Synchronous transmission
- Transmission lengths to 1000 feet
- Accepts clock rates from 100 KHz to 1.8 MHz

Data Transmission Sequence

1. Output driver of the encoder is a MAX 491 transceiver in transmit mode. The recommended receiver is a MAX 491 transceiver in receive mode.
2. Controller provides a series of pulses (or differential pulse pairs) on the CLOCK input lines.
3. On the first HIGH-to-LOW CLOCK transition, the encoder latches its data at the current position and prepares to transmit.
4. Controller reads data on the falling edge of the next 16 clock cycles.
5. The first bit is a START bit and is always HIGH.
6. Next come 12 data bits beginning with the most significant bit (MSB) and ending with the least significant bit (LSB). This is followed by three LOW pulses.
7. After the DATA bits, the DATA line goes LOW and remains LOW for a minimum of 30 microseconds between the end of the DATA bits and the beginning of the next CLOCK series.

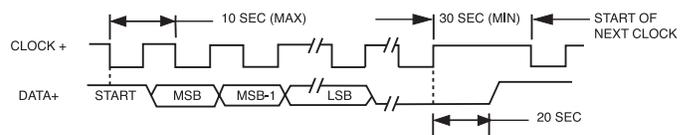
Interfacing Long Data Lines

Cable impedance can create a transmission delay, in effect, shifting the phase relationship between the clock pulse and the data. If this phase shift exceeds 180°, then the wrong bit position will be sampled by the receiver. As a result, the maximum allowable clock frequency is a function of the cable length. For 24 AWG, stranded, 3 pair cable (BEI part number 37048-003 or equivalent) the group delay is 1.36ns/ft. The table below shows the maximum transmission rate allowable as a function of cable length to ensure a phase shift of less than 90°.

CLOCK, Maximum (kHz) = 92,000 / Cable Length (ft)CW

Cable Length (ft)	50	100	200	300	500	1000
Max Freq (kHz)	1800	900	500	300	200	100

SSI Timing



Ordering SSI

HOW TO SPECIFY SSI OUTPUT IN THE ENCODER MODEL NUMBER:

Use the designation, S3 between the **Code Format** designation and the **Connector** designation.

Example: H25D-SS-12GC-S3-CW-SM18



Single Turn Absolute Encoder Options

The tables below are reference for pinouts, connections and operation of BEI's single turn absolute encoders. These absolute options are available in a wide range of package styles with a variety of outputs. The applicability table below shows which combinations are currently available. As always, you can call us at **800-360-ASAP (2727)** for immediate applications assistance should you have any questions.

Output Code and Terminations (12 & 13 Bit)								
PARALLEL CODE					TERMINATION TYPE			
	Gray Code		Natural Binary		Binary Coded Decimal	Cable	Conn	Term Board H38 & H40
	12 Bit	13 Bit	12 Bit	13 Bit				
MSB	G ₁₁	G ₁₂	2 ¹¹	2 ¹²	A ₀	WHT/BLK	A	1
	G ₁₀	G ₁₁	2 ¹⁰	2 ¹¹	B ₀	WHT/BRN	B	2
	G ₉	G ₁₀	2 ⁹	2 ¹⁰	C ₀	WHT/RED	C	3
	G ₈	G ₉	2 ⁸	2 ⁹	D ₀	WHT/ORN	D	4
	G ₇	G ₈	2 ⁷	2 ⁸	A ₁	WHT/YEL	E	5
	G ₆	G ₇	2 ⁶	2 ⁷	B ₁	WHT/GRN	F	6
	G ₅	G ₆	2 ⁵	2 ⁶	C ₁	WHT/BLU	G	7
	G ₄	G ₅	2 ⁴	2 ⁵	D ₁	WHT/IO	H	8
	G ₃	G ₄	2 ³	2 ⁴	A ₂	WHT/GRY	J	9
	G ₂	G ₃	2 ²	2 ³	B ₂	WHT	K	10
LSB ₁₂	G ₁	G ₂	2 ¹	2 ²	C ₂	GRY/BLK	L	11
	G ₀	G ₁	2 ⁰	2 ¹	D ₂	GRY/BRN	M	12
LSB ₁₃		G ₀		2 ⁰	A ₃	GRY/RED	N	13
*0V (CIRCUIT COMMON)					B ₃	GRY/RED	P	
DIRECTION CONTROL						ORN	R	18
CASE GROUND						GRN	S	16
0 V (CIRCUIT COMMON)						BLK	T	15
LATCH CONTROL						YEL	U	17
+V (SUPPLY VOLTAGE)						RED	V	14
SHIELD DRAIN						BARE	—	

*Pin P is available for a tri-state option

Output Applicability Table							
	12 BITS	13 BITS	14/15 BITS	12x12 BITS	SSI	4-20 mA	0-10 V
H25	•	•			•	•	•
H25X			•				
HS35	•				•		
H38	•	•		•	•	•	•
H40	•	•		•	•	•	•
HMT25				•	•		

Direction Control: Standard is CW increasing when viewed from the shaft end. Pin R is normally HI (or N/C) and is pulled up internally to +V. To reverse the count direction, Pin R must be pulled LO (COMMON).

Latch control: Encoder outputs are active and provide continuous parallel position information when Pin U is HI (or N/C). Pin U is pulled up internally to +V. When Pin U is LO (COMMON) the encoder outputs are latched at the logic state that is present when the latch is applied and will stay latched until Pin U is no longer grounded.

Dir/Latch on 15-Bit Encoders: Due to a limited number of connector pins, either direction control or latch is available on pin U.

Parallel Code (14 & 15 Bit)					
	Gray Code		Natural Binary		M14/19 Connector
	14 BIT	15 Bit	14 BIT	15 Bit	
LSB	G ₀	G ₀	2 ⁰	2 ⁰	A
	G ₁	G ₁	2 ¹	2 ¹	B
	G ₂	G ₂	2 ²	2 ²	C
	G ₃	G ₃	2 ³	2 ³	D
	G ₄	G ₄	2 ⁴	2 ⁴	E
	G ₅	G ₅	2 ⁵	2 ⁵	F
	G ₆	G ₆	2 ⁶	2 ⁶	G
	G ₇	G ₇	2 ⁷	2 ⁷	H
	G ₈	G ₈	2 ⁸	2 ⁸	J
	G ₉	G ₉	2 ⁹	2 ⁹	K
	G ₁₀	G ₁₀	2 ¹⁰	2 ¹⁰	L
	G ₁₁	G ₁₁	2 ¹¹	2 ¹¹	M
	G ₁₂	G ₁₂	2 ¹²	2 ¹²	N
MSB14	G ₁₃	G ₁₃	2 ¹³	2 ¹³	P
MSB15	DIR CONTROL	G ₁₄	DIR CONTROL	2 ¹⁴	R
	CASE GROUND	CASE GROUND	CASE GROUND	CASE GROUND	S
	CIRCUIT COMMON	CIRCUIT COMMON	CIRCUIT COMMON	CIRCUIT COMMON	T
	LATCH	DIR/LATCH	LATCH	DIR/LATCH	U
	+V SUPPLY VOLTAGE	+V SUPPLY VOLTAGE	+V SUPPLY VOLTAGE	+V SUPPLY VOLTAGE	V

SSI Output Termination Table					
	M18 CONN	M14/19 CONN	CABLE CONN	TERM. BOARD	
				H38	H48
DATA +	A	A	YEL	4	1
DATA-	H	B	WHT/YEL	7	7
CLOCK+	B	C	BLU	5	2
CLOCK-	I	D	WHT/BLU	8	8
DIR CONTROL	C	R	ORN	6	3
CASE GROUND	G	S	BARE/SHIELD	1	6
CIRCUIT COMMON	F	T	BLK	2	5
+V SUPPLY VOLTAGE	D	V	RED	3	4
SHIELD DRAIN	—	—	BARE	—	—

M18 Connector is a MS3102E18-1P, 10-pin connector on the encoder body and mates to an MS3106F18-1S connector or can be used with a standard cable/connector assembly, BEI P/N 924-31186-18XX (Where X = 10, 20 or 30 for a 10, 20, or 30 foot length). This is the preferred connector for SSI output.

M14/19 Connector is a MS3112E14-19P, 19-pin connector on the encoder body and mates to an MS3114E14-19S or equivalent.