

RETICON[®]

Application Note #127

Clock Drivers for P-series Linear Photodiode Array Imagers



Description

For driving P-series linear CCD sensors, PerkinElmer recommends the following drivers. Each driver has conditions listed with it; consult the appropriate driver datasheet for more specific information regarding use and specifications.

Table 1. Driver Matrix						
	\emptyset_{TG}	\emptyset_{PG}	\emptyset_{AB}	\emptyset_{H1}	\emptyset_{H2}	\emptyset_{RG}
14μm 1 output	Vishay TelCom	Vishay TelCom	Vishay TelCom	Pericom	Pericom	Pericom ON ¹
14μm 2 output	Vishay TelCom	Vishay TelCom	Vishay TelCom	Pericom	Pericom	Pericom ON ¹
14μm 4 output	Vishay TelCom	Vishay TelCom	Vishay TelCom	Pericom Elantec	Pericom Elantec	Pericom Elantec
7μm 2 output	Vishay TelCom	Vishay TelCom	Vishay TelCom	Elantec	Elantec	Elantec
7μm 4 output	Vishay TelCom	Vishay TelCom	Vishay TelCom	Elantec	Elantec	Elantec

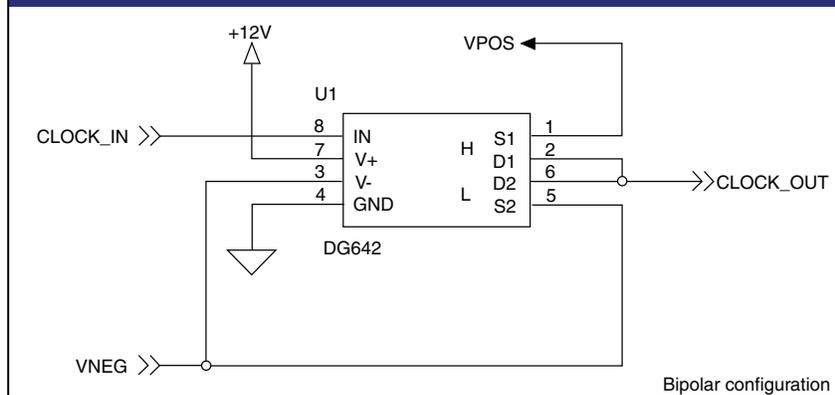
Note 1. When driving the 14 μ m 1 or 2 output sensors \emptyset_{RG} at a 40MHz 25% duty clock cycle, use the ON Semiconductor driver only. This is due to the 6ns minimum pulse width of the Pericom driver.

Driver Application Note

Vishay Driver Configuration

The Vishay driver is based on an analog switch (Vishay DG642). It provides bipolar clock outputs (i.e. -5V to 5V) up to 10 MHz at a 50% duty cycle clock. It is to be used on \emptyset_{TG} , \emptyset_{PG} , and \emptyset_{AB} . Unipolar clock outputs (0V to 5V) are possible if Vneg is set to GROUND. This driver can be used with all P-series sensors. This driver will use fewer components and less board space than the TelCom, but will be more expensive. The CLOCK_IN is TTL. See Figure 1 for configuration.

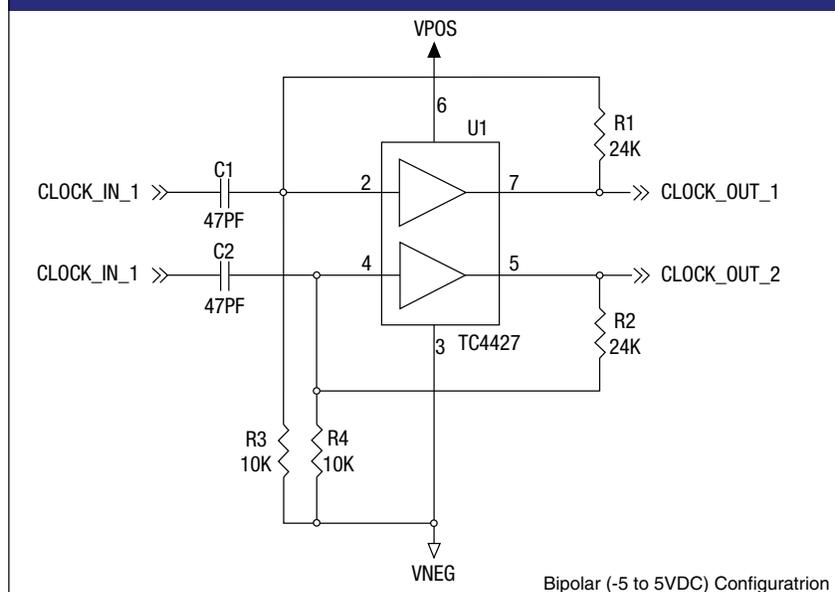
Figure 1. Vishay Driver Configuration



TelCom Driver Configuration

This TelCom driver is based on a MOSFET driver (TelCom TC4427). It provides bipolar clock outputs (i.e. -5V to 5V) up to 10 MHz at a 50% duty cycle clock. It is to be used on \emptyset_{TG} , \emptyset_{PG} , and \emptyset_{AB} . Unipolar clock outputs (0V to 5V) are possible if Vneg is set to GROUND. It has a maximum of 18V at 1.5A. This driver can be used with all P-series sensors. This driver will use more components and board space than the Vishay, but will be less costly. In unipolar configuration with TTL inputs, C1-C2 and R1-R4 are not needed. R1-R4 are selected for -5 to +5VDC output clocks. The CLOCK_IN is TTL. See Figure 2 for configuration.

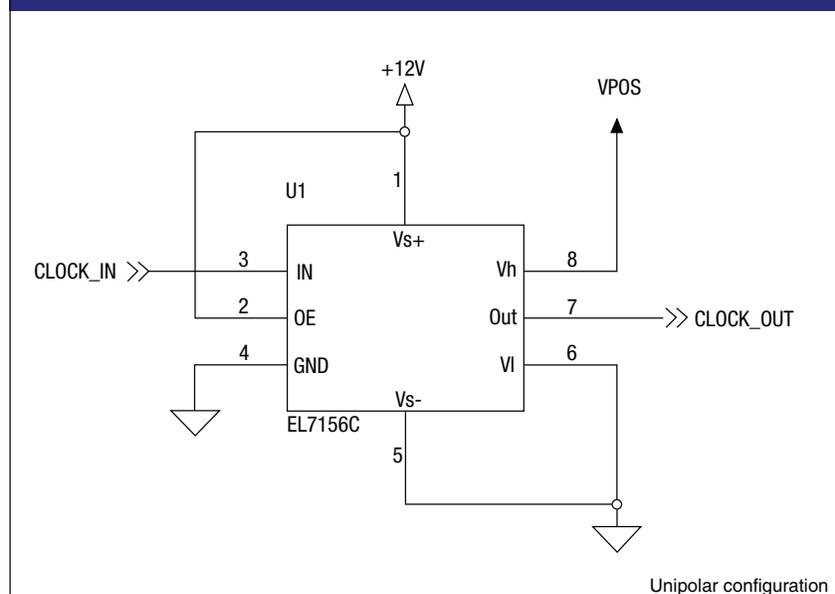
Figure 2. TelCom Driver Configuration



Elantec Driver Configuration

This driver is based on an Elantec CCD driver (EL7156C). It will provide unipolar clock outputs up to a 40 MHz 50% duty cycle clock. It is used in \emptyset_{H1} , \emptyset_{H2} , and \emptyset_{RG} . Bipolar circuit configurations are also possible. The maximum voltage limit ($V_{pos} - V_{neg}$) is 15V. This driver is recommended for the P-series 7 μ m sensors, as well as the 14 μ m 4 output devices. The CLOCK_IN is TTL. See Figure 3 for configuration.

Figure 3. Elantec Driver Configuration

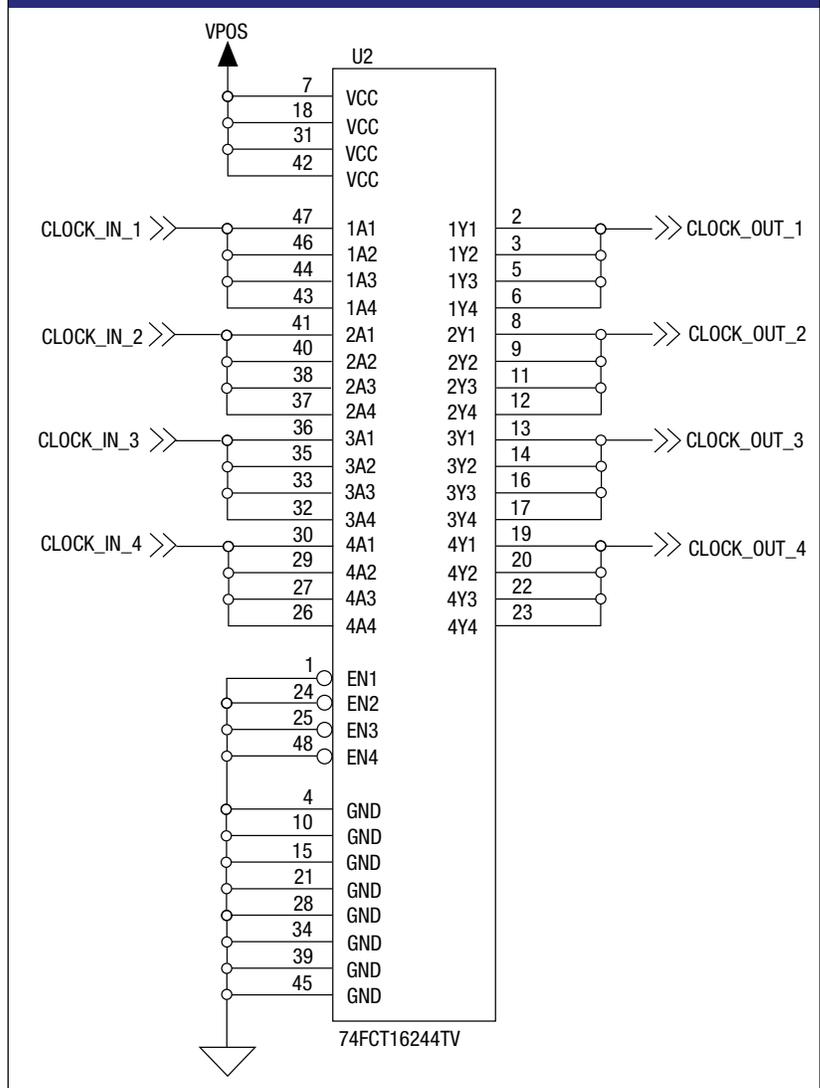


Driver Application Note

Pericom Driver Configuration

This driver is based on a Pericom TTL logic buffer (PI74FCT16244). It will provide unipolar clock outputs up to a 40MHz 50% duty cycle. It is used in ϕ_{H1} , ϕ_{H2} , and ϕ_{RG} . Bipolar circuit configurations are not possible with this driver. V_{pos} is limited to 4.5V to 5.5V. This part is recommended for all P-series 14 μ m 1, 2, and 4 output devices. The configuration shown uses parallel outputs to increase current drive capacity. Four parallel buffers will drive an 8192 pixel sensor at 20MHz. See Figure 4 for configuration.

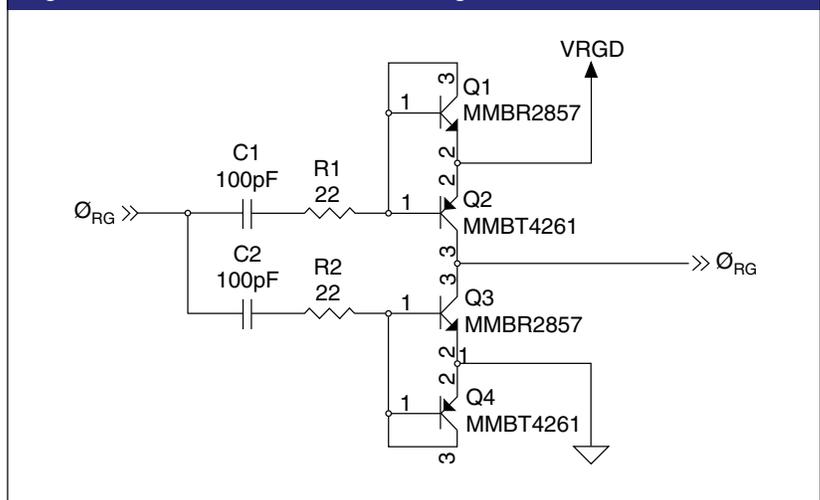
Figure 4. Pericom Driver Configuraiton



ON Semiconductor Driver Configuration

This driver is based on a matched pair of ON Semiconductor (formerly Motorola) RF switching transistors. This is to be used exclusively with the P-series 1 and 2 output sensors when driving ϕ_{RG} at 40 MHz. The circuit shown is a "push-pull" transistor configuration. Other RF switching transistors are possible. Q1 and Q4 can be replaced with diodes. ϕ_{RG} is TTL. See Figure 5 for configuration.

Figure 5. On Semiconductor Driver Configuration



Driver Application Note

ESD Warning

While P-series imagers have been designed to resist electrostatic damage (ESD), they can be damaged from such discharges. Always observe proper ESD precautions when handling and storing these imagers.

Table 2. Web Links for Data Sheets

Elantec	http://www.elantec.com
On	http://www.onsemi.com
Pericom	http://www.pericom.com
TelCom	http://www.telcom-semi.com
Vishay	http://www.Vishay.com

Trace Length

All drivers should be located no more than a 1 inch trace from the sensor pin. Additionally, it is recommended that faster drivers be located closer to the sensor than slower drivers. Traces should always be as short as possible.

For more information e-mail us at opto@perkinelmer.com or visit our web site at www.perkinelmer.com/opto. All values are nominal; specifications subject to change without notice.

While the information provided in this application note is intended to describe the form, fit, and function of this product, PerkinElmer reserves the right to make changes without notice. Additionally, users are cautioned to always verify the function and operation of these drivers with the manufacturer before use.

Table 3. Sales Offices

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United States	North America PerkinElmer Optoelectronics 2175 Mission College Blvd. Santa Clara, CA 95054 Toll Free: 800-775-OPTO (6786) Phone: +1-408-565-0830 Fax: +1-408-565-0703
Germany	Europe PerkinElmer Optoelectronics GmbH Wenzel-Jaksch-Str. 31 D-65199 Wiesbaden, Germany Phone: +49-611-492-570 Fax: +49-611-492-165
Japan	Asia PerkinElmer Optoelectronics NEopt. 18F, Parale Mitsui Building 8 Higashida-Cho, Kawasaki-Ku Kawasaki-Shi, Kanagawa-Ken 210-0005 Japan Phone: +81-44-200-9170 Fax: +81-44-200-9160 www.neopt.co.jp
Singapore	47 Ayer Rajah Crescent #06-12 Singapore 139947 Phone: +65-770-4925 Fax: +65-777-1008

